

Day : Wednesday

Date: 11/23/2005

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Inventor Information for 10/663354

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|-------|--------|---|---|------------------|---------|------------------|
| L1 | 7783 | resistor and "338"/\$.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 15:23 |
| L2 | 32648 | substrate and protective and film and bonding | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 15:24 |
| L3 | 149 | 1 and 2 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 15:24 |
| L4 | 242141 | flush | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 15:24 |
| L5 | 7 | 3 and 4 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 15:55 |
| L6 | 50312 | thick adj film | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 15:55 |
| L7 | 47133 | etching and etchant | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 15:55 |
| L8 | 1757 | 6 and 7 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 15:55 |
| L9 | 5 | 3 and 8 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 16:06 |
| L10 | 157627 | arrangement and resistor | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 16:07 |

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|-----|---------|------------------------|---|----|----|------------------|
| L11 | 1591321 | semiconductor | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 16:07 |
| L12 | 2744 | alternate near display | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 16:07 |
| L13 | 4579 | reduce near coupling | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 16:07 |
| L14 | 0 | 1 and 10 and 11 and 12 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 16:08 |
| L15 | 2 | 1 and 10 and 11 and 13 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 16:09 |
| L16 | 0 | 1 and 10 and 12 and 13 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/23 16:09 |